

SPECIFICATIONS

PC104-DAC06

Analog Outputs



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Power Consumption

I _{cc} : +5V quiescent	130 mA typical, 180 mA maximum
I _{cc} : +12V quiescent	50 mA typical, 75 mA maximum
I _{cc} : -12V quiescent	30 mA typical, 45 mA maximum

Analog Output section

D/A converter type	AD7237
Resolution	12 bits
Number of channels	6
Ranges	±10V, ±5V, 0 to 10V, 0 to 5V; each channel individually jumper-selectable
D/A pacing	Software
Data transfer	Software-pollled
Throughput	125 kHz typical (PC-dependent)
Offset error	Adjustable to zero
Gain error	Adjustable to zero
Integral non-linearity	±0.5 LSB
Differential non-linearity	±0.5 LSB
Monotonicity	Guaranteed over temperature range
Gain drift	160 ppm/°C
Zero drift	150 ppm/°C
Current Drive	±5 mA minimum
Short circuit current	±40 mA
Output resistance	0.1ohms
Slew rate	1.7V/μs
Miscellaneous	Double buffered input latches Update DACs individually or simultaneously (jumper-selectable by DAC pairs) DAC output state on power-up and reset undefined

Environmental

Operating temperature range	0 to 70°C
Storage temperature range	-40 to 100°C
Humidity	0 to 95% non-condensing

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